


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [All](#)

Welcome United States Patent and Trademark Office

Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "((scalar <and> vector <and> (one , single) <near/4> chip* <and> (multim...

e-mail

Your search matched 3 of 1332769 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

[View Session History](#)[New Search](#)

Modify Search

((scalar <and> vector <and> (one , single) <near/4> chip* <and> (multimedia, sim

[Search](#) >☐ Check to search only within this results set

Display Format:



Citation



Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

[view selected items](#)[Select All](#) [Deselect All](#)**1. The impact of Mpack 2**

Purcell, S.;

[Signal Processing Magazine, IEEE](#)

Volume 15, Issue 2, March 1998 Page(s):102 - 107

Digital Object Identifier 10.1109/79.664703

[AbstractPlus](#) | Full Text: PDF(612 KB) IEEE JNL[Rights and Permissions](#)**2. Video DSP architecture for MPEG2 codec**

Araki, T.; Toyokura, M.; Akiyama, T.; Takeno, H.; Wilson, B.; Aono, K.;

[Acoustics, Speech, and Signal Processing, 1994. ICASSP-94, 1994. IEEE International Conference](#)

Volume II, 19-22 April 1994 Page(s):IV/417 - IV/420 vol.2

Digital Object Identifier 10.1109/ICASSP.1994.389632

[AbstractPlus](#) | Full Text: PDF(288 KB) IEEE CNF[Rights and Permissions](#)**3. A236 parallel DSP chip provides real-time video processing economically and efficiently**

Morton, S.G.;

[ELECTRO '96, Professional Program, Proceedings,](#)

30 April-2 May 1996 Page(s):261 - 268

Digital Object Identifier 10.1109/ELECTR.1996.501237

[AbstractPlus](#) | Full Text: PDF(628 KB) IEEE CNF[Rights and Permissions](#)[Help](#) [Contact Us](#) [Privacy](#)

© Copyright 2006 IEEE

 indexed by